

2. (Twice Amended) A card with a microprocessor and contacts according to Claim 1, wherein the communication device comprises:

- a circuit for analysing electrical signals transmitted by the terminal so as to supply a series of electrical pulses,

- a circuit for checking the series of electrical pulses in order to determine the integrity of the series of electrical pulses and to supply a code indicating the status of the check,

- a circuit for determining each character from the pulses in the series,

- a first plurality of registers for recording characters of a command and an address supplied by the character determination circuit and making them available to the microprocessor,

- a second plurality of registers for recording characters of data supplied by the character determination circuit and making them available to the microprocessor,

- a circuit for acknowledging the command, associated with the first plurality of registers, for analysing the characters of the command and supplying a code indicating a command reception status,

- a third plurality of registers for recording codes for the data and for the status of execution of the command supplied by the microprocessor, and

- a circuit for transmitting to the terminal the codes supplied by the checking circuit, the command acknowledgement circuit and the third plurality of registers.

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contd.

3. (Twice Amended) A card with microprocessor and contacts according to Claim 2, wherein the analysis circuit detects the signals transmitted and presents them in the form of a series of binary electrical pulses.

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cont.

4. (Twice Amended) A card with a microprocessor and contacts according to Claim 2 wherein the checking circuit checks for a binary parity digit or a cyclic redundancy code and supplies a corresponding signal or code.

5. (Amended) A card with a microprocessor and contacts card according to Claim 3, wherein the checking circuit checks for a binary parity digit or a cyclic redundancy code and supplies a corresponding signal or code.

IN THE ABSTRACT:

Replace the Abstract on page 10 with the following:

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The invention relates to cards with a microprocessor and contacts. The invention lies in the fact that a communication device of the asynchronous type is disposed between the contacts and the microprocessor so as to relieve the microprocessor of the communication tasks and thus allow better use of the central unit of the microprocessor and the associated memories. This device includes an analysis circuit, a circuit for checking the integrity of the series of pulses, a circuit for determining the characters in the series of pulses and pluralities of registers which are connected with the microprocessor.
